

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (USPTO)

Serial Number	10/781,336
Confirmation Number	9427
Filing Date	February 17, 2004
Title of Application	METHOD AND SYSTEM FOR MULTI-PROCESSOR FFT/IFFT WITH MINIMUM INTER-PROCESSOR DATA COMMUNICATION
First Named Inventor	Kaushik SAHA et al.
Assignee	STMicroelectronics Pvt. Ltd.
Group Art Unit	2193
Examiner	DO, Chat C.
Attorney Docket Number	2415-015-03
Nature of the Office Communication to which this is responding	Notice of Allowance
Date of the Office Communication	January 27, 2010
Nature of this Document	Comments on Statement of Reasons for Allowance

CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being transmitted via the Office electronic filing system, EFS-Web, addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 27th day of April, 2010.

/Rosanne F. Chow/
Rosanne F. Chow

COMMISSIONER FOR PATENTS:

In response to the Notice of Allowability in this case mailed January 27, 2010, the Applicants, acting through their attorney, comment as follows. The Applicants thank the Examiner for allowing the claims. But the Applicants do not necessarily agree with the Examiner's Statement of Reasons for Allowance, and respectfully submit that the claims may be allowable for other reasons.

In the event additional fees are due as a result of this document, payment for those fees are enclosed. Should further payment be required to cover such fees you are hereby authorized to charge such payment to Deposit Account No. 07-1897.

If the Examiner believes that a phone interview would be helpful, he is respectfully requested to contact the Applicants' attorney, Paul F. Rusyn, at (425) 455-5575.

Respectfully Submitted,

Graybeal Jackson LLP
Representatives for Applicant

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Dated: April 27, 2010

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